

Refine Search

Search Results -

Term	Documents
(25 AND 9)[CCLS].PGPB,USPT.	0
((L9 AND L25) [CCLS]).PGPB,USPT.	0

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L27

Refine Search

Recall Text

Clear

Interrupt

Search History

 DATE: Thursday, May 12, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
side by side			
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L27</u>	(19 and L25) [CCLS]	0	<u>L27</u>
<u>L26</u>	19 and L24	46	<u>L26</u>
<u>L25</u>	(712/228)[CCLS]	511	<u>L25</u>
<u>L24</u>	(712/228)[CCLS]	511	<u>L24</u>
<u>L23</u>	(712/2-300)[CCLS]	10959	<u>L23</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L22</u>	4740893.pn.	2	<u>L22</u>
<u>L21</u>	L19 near15 context\$3 near4 switch\$5	56	<u>L21</u>
<u>L20</u>	L19 near28 context\$3 near4 switch\$5	60	<u>L20</u>
<u>L19</u>	(detect\$4 or check\$4 or determin\$7 or indicat\$5) near10 (updat\$6 or modif\$6 or chang\$5 or add\$5 or refresh\$5 or stor\$4 or sav\$3) near10 register\$1	50537	<u>L19</u>

<u>L18</u>	L17 not l16	1	<u>L18</u>
<u>L17</u>	L14 near55 context\$3 near4 switch\$5	7	<u>L17</u>
<u>L16</u>	L14 near35 context\$3 near4 switch\$5	6	<u>L16</u>
<u>L15</u>	L14 near28 context\$3 near4 switch\$5	6	<u>L15</u>
<u>L14</u>	(detect\$4 or check\$4 or determin\$7 or indicat\$5) near10 (updat\$6 or modif\$6 or chang\$5) near10 register\$1	12351	<u>L14</u>
<u>L13</u>	L12 near25 context\$3 near4 switch\$5	4	<u>L13</u>
<u>L12</u>	(detect\$4 or check\$4 or determin\$7 or indicat\$5) near8 (updat\$6 or modif\$6) near8 register\$1	4963	<u>L12</u>
<u>L11</u>	L9 near25 context\$3 near4 switch\$5	1	<u>L11</u>
<u>L10</u>	L9 near15 context\$3 near4 switch\$5	1	<u>L10</u>
<u>L9</u>	indicat\$5 near8 (updat\$6 or modif\$6 or chang\$3) near7 register\$1	4536	<u>L9</u>
<u>L8</u>	L7 not l5	3	<u>L8</u>
<u>L7</u>	L6 near25 context\$3 near4 switch\$5	5	<u>L7</u>
<u>L6</u>	(set\$4 or assert\$4 or deassert\$4 or reset\$4 or indicat\$5) near8 (updat\$6 or modif\$6 or chang\$3) near12 bit\$1	16660	<u>L6</u>
<u>L5</u>	L3 near25 context\$3 near4 switch\$5	2	<u>L5</u>
<u>L4</u>	L3 near17 context\$3 near4 switch\$5	2	<u>L4</u>
<u>L3</u>	indicat\$5 near8 (updat\$6 or modif\$6 or chang\$3) near12 bit\$1	8249	<u>L3</u>
<u>L2</u>	L1 near15 context\$3 near4 switch\$5	1	<u>L2</u>
<u>L1</u>	indicat\$5 near5 (updat\$6 or modif\$6) near8 bit\$1	2729	<u>L1</u>

END OF SEARCH HISTORY